APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/618,060	07/11/2003	Robert Jackson	15114H-068300US	4329

			EXAMINER
26059	7590	06/06/2005	DINH, PAUL

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ART UNIT	PAPER NUMBER
2825	

DATE MAILED: 06/06/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
Office Action Summary	10/618,060	JACKSON, ROBERT
	Examiner	**Art Unit**	
	Paul Dinh	2825	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>3</u> MONTH(S) FROM
THE MAILING DATE OF THIS COMMUNICATION.
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed
  after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
  Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any
  earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on <u>11 May 2005</u>.

2a)☒ This action is **FINAL**.   2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is
closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) <u>1-15</u> is/are pending in the application.

    4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) <u>1-15</u> is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on <u>11 July 2003</u> is/are: a)☒ accepted or b)☐ objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a)☐ All   b)☐ Some * c)☐ None of:

        1.☐ Certified copies of the priority documents have been received.

        2.☐ Certified copies of the priority documents have been received in Application No. _____.

        3.☐ Copies of the certified copies of the priority documents have been received in this National Stage
application from the International Bureau (PCT Rule 17.2(a)).

    * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)

2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.

4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ .

5) ☐ Notice of Informal Patent Application (PTO-152)

6) ☐ Other: _____.

DETAILED ACTION

This FINAL OFFICE ACTION is a response to the amendment + remarks filed on 5/11/05.

Claims 1-15 are pending

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) The invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-15 are rejected under 35 U.S.C. 102(e) as being anticipated by Gemelli et al (US Patent Publication No.: 2003/0101307

(Claim 1 and similarly recited claim 9)

Defining respective functional representations of a plurality of system components (one or more of: *bus (es), bus widths, bus length, number of bits on bus, bus devices, bus components, bus configurations, peripherals modules in fig 2-25, 28-54*), each functional representation including at least one parameter value (*parameter value = bus width or number of bits on bus*); and

A processor (in one or more of: fig 3-15, 17-26, CAD system (para 0002)) automatically defining an allowable set of such parameter values in dependence upon the plurality of system components (fig 3-15, 17-26: *bus widths or number of bits on bus in dependence upon the plurality of system components (buses), i.e., bus widths depend on the number of bits and/or data/address lines and/or the number of conductors of/in busses)*, wherein the allowable set of parameter values includes at least one common [compatible] parameter value (*fig 12, 14, 17-24 (common parameter value = bus width of the common bus or the number of bits configured for the bus of the common bus)* from the respective functional representations of at least two of the plurality of the components (*functional representations of at least two of the plurality of the components = bus widths or the numbers of bits (i.e., 8 bits, 16 bits, 32 bits) for the buses of bus devices, of bus components, of peripherals modules, of bus-related elements connecting/interconnecting/sharing the common bus as shown in fig 12, 14, 17-24)*

(Claim 6)

A plurality of system components (fig *2-25, 28-54*), a model including a functional representation of the component concerned which representation includes at least one parameter value *[(one or more of: bus (es) (abstract, fig 9-11, 17, 29-50), bus widths, bus length, number of bits on bus, bus devices, bus components, bus configurations, peripherals modules in fig 2-25, 28-54)]* for the component, <u>wherein the parameter value relates to a data transfer protocol</u> (abstract, fig 9-11, 17, 29-50)<u> operation associated with the component.</u>

(Claims 2, 10) wherein one of the system components is a bus (*see bus in fig 2-25, 28-54*).

(Claims 3, 8, 11) wherein the functional representation of the bus includes a parameter value relating to bus width (see bus width in fig 12, 14-25, 34-35, 43-49)

(Claim 4) further comprising choosing an allowable set of parameter values and setting the parameter values of the functional representations concerned to the values defined by the chosen allowable set of parameter values (common/compatible *bus widths, bus length, number of bits on common bus, on bus devices, on bus components, bus configurations, on peripherals modules in fig 2-25, 28-54*)

(Claims 5, 13) selecting a plurality of system components *(one or more of: bus (es), bus widths, bus length, number of bits on bus, bus devices, bus components, bus configurations, peripherals modules in fig 2-25, 28-54)*; selecting a connection for interconnecting *(lines/conductors/data in/between buses/components/peripherals/module)* such selected system components; and

Selecting one of the allowable sets of parameter values (one or more of: *bus (es), bus widths, bus length, number of bits on bus, bus devices, bus components, bus configurations, peripherals modules in fig 2-25, 28-54)* in dependence upon said connection (*bus width and/or number of bits in a bus in dependence upon bus connection, i.e., depend on the number of bits and/or data lines and/or the number of conductors of/in busses*)

(Claim 7) wherein the <u>functional representation</u> <u>includes</u> a parameter <u>value</u> relates to <u>a</u> data transfer [characteristics] <u>protocol role</u> (abstract, fig 9-11, 17, 29-50) of the component.

(Claim 12) wherein the processor is operable to choose an allowable set of parameter values (*common/compatible bus widths, bus length, number of bits on common bus, on bus devices, on bus components, bus configurations, on peripherals modules in fig 2-25, 28-54)* and setting the parameter values of the functional representations concerned *to* the values defined by the chosen allowable set of parameter values (*common/compatible bus widths, bus length, number of bits on common bus, on bus devices, on bus components, bus configurations, on peripherals modules in fig 2-25, 28-54)*

(Claims 14-15) a PLD *(fig 5-11, title, abstract, field of invention, background, summary)*

Applicant's arguments with respect to claims 1-15 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL.** See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paul Dinh whose telephone number is 571-272-1890. The examiner can normally be reached on Monday to Friday from 8:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on 571-272-1907. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Paul Dinh

Patent Examiner

VUTHE SIEK
PRIMARY EXAMINER